

MARKUP VERSION OF THE AMENDED CLAIMS

(Claims 12-15, 23 and 25 have been amended as follows)

12. (Once Amended) A computing apparatus as recited in claim [11] 15, wherein said clock control unit operates to decrease the clock frequency when the temperature of said processing unit is too high.

13. (Once Amended) A computing apparatus as recited in claim [11] 15, wherein said computing apparatus is a microprocessor.

14. (Once Amended) A computing apparatus as recited in claim [11] 15, wherein said computing apparatus is a computer system.

15. (Once Amended) A computing apparatus [as recited in claim 11, wherein said computing apparatus further comprises:], comprising:

a processing unit, said processing unit executes instructions in accordance with a clock signal having a clock frequency;

a temperature sensor that monitors temperature of said processing unit;

an activity detector that monitors activity of said processing unit; and

a clock control unit operatively connected to said processing unit and said temperature sensor and said activity detector, said clock control unit operates to alter the clock frequency of the clock signal in a gradual and dynamic manner based on the temperature of said processing unit as monitored by said temperature sensor [, wherein said clock control unit is operatively connected to said processing unit, said temperature sensor and said activity detector, and wherein said clock control unit operates to alter the clock frequency of the clock signal in a gradual and dynamic manner based on the temperature of said processing unit as monitored by said temperature sensor] and on the activity of said processing unit as monitored by said activity detector.

23. (Once Amended) A computer as recited in 22, wherein the reduced power mode includes at [lea] least a sleep state.

25. (Once Amended) A computer as recited in claim 22, wherein said fan controller uses pulse [widt] width modulation to control the speed of said fan.